REMARKS

The specification has been amended to make editorial changes to place the application in condition for allowance at the time of the next Official Action.

Claims 1-18 were previously pending in the application. Claims 1-18 are cancelled and replaced with new claims 19-36.

Claims 1-6, 8-10, 12-14, 16 and 17 are rejected as anticipated by KREGNESS et al. 4,984,153.

Reconsideration and withdrawal of the rejection are respectfully requested because the reference does not disclose or suggest that once each of a plurality of processors has accessed a shared memory, when one of the plurality of processors updates a predetermined data in the shared memory, said one of the plurality of processors requests others of said plurality of processors to access said updated predetermined data from the shared memory as recited in new claim 19.

Column 4, lines 40-52 of KREGNESS et al. teaches that upon gaining control of the locking cell, the requesting processor is free to access the word, perform the required test, and if successful, modify the word. Control is then released over the locked address so that another processor may gain control and perform its test. Such a teaching is a simultaneous access and update for each computer in turn. Neither this passage nor any other passage of KREGNESS et al. teaches that once each of the plurality of processors has accessed the shared memory,

one of the plurality of processors requests others of the processors to access the updated predetermined data from the shared memory as recited in claim 19.

As the reference does not disclose that which is recited, the anticipation rejection is not viable. Reconsideration and withdrawal of the rejection are respectfully requested.

Independent claims 22, 26, 30, 34 and 35 include similar recitations and thus these claims and the claims that depend therefrom are also believed patentable over KREGNESS et al.

Claims 7, 11 and 15 are rejected as unpatentable over KREGNESS et al. in view of HAUCK et al. 6,587,904. This rejection is respectfully traversed.

HAUCK et al. is only cited for the teaching that if arbitration is not won within the specified amount of time, then a long bus reset is requested. HAUCK et al. do not teach or suggest what is recited in claims 22, 26 or 30. As set forth above, KREGNESS et al. do not disclose or suggest what is recited in claims 22, 26 and 30. Since claims 7, 11 and 15 are rewritten as claims 25, 29 and 33 and depend from claims 22, 26 and 30, respectively, and further define the invention, the proposed combination of references would not render obvious new claims 25, 29 and 33.

Claim 18 is rejected as unpatentable over KREGNESS et al. in view of MOGUL 6,704,798. This rejection is respectfully traversed.

Claim 18 corresponds to new claim 36, which provides wherein, once each of said plurality of processors has accessed said shared memory, said one processor updates a predetermined data in said shared memory and requests said others of said plurality of processors to access said updated predetermined data from said shared memory.

As set forth above with respect to claim 19, KREGNESS et al. teach at column 4, lines 40-52 that a processor gains control, performs a required test and then control is released to another processor so that the other processor can gain control and perform its test. KREGNESS et al. do not teach or suggest that once each of said plurality of processors has accessed said shared memory, said one processor updates a predetermined data in said shared memory and requests said others of said plurality of processors to access said updated predetermined data from said shared memory as recited in new claim 36.

MOGUL is only cited for the teaching of implementing a data signal embedded in a carrier wave and representing an instruction sequence. MOGUL does not teach or suggest that once each of said plurality of processors has accessed said shared memory, said one processor updates a predetermined data in said shared memory and requests said others of said plurality of

processors to access said updated predetermined data from said shared memory as recited in new claim 36.
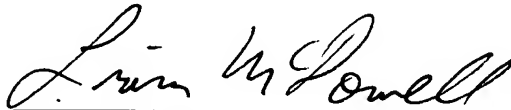
The above-noted feature is missing from each of the references, is absent from the combination and thus is not obvious to one having ordinary skill in the art.

In view of the present amendment and the foregoing remarks, it is believed that the present application has been placed in condition for allowance. Reconsideration and allowance are respectfully requested.

The Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 25-0120 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

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